

FIG.2

NOP	NOP	NOP	NOP
E1	NOP	NOP	NOP
NOP	E1	NOP	NOP
NOP	NOP	E1	NOP
NOP	NOP	NOP	E1
E1	E1	NOP	NOP
E1	NOP	E1	NOP
E1	NOP	NOP	E1
NOP	E1	E1	NOP
NOP	E1	NOP	E1
NOP	NOP	E1	E1
E1	E1	E1	NOP
E1	E1	NOP	E1
E1	NOP	E1	E1
NOP	E1	E1	E1
E1	E1	E1	E1

į

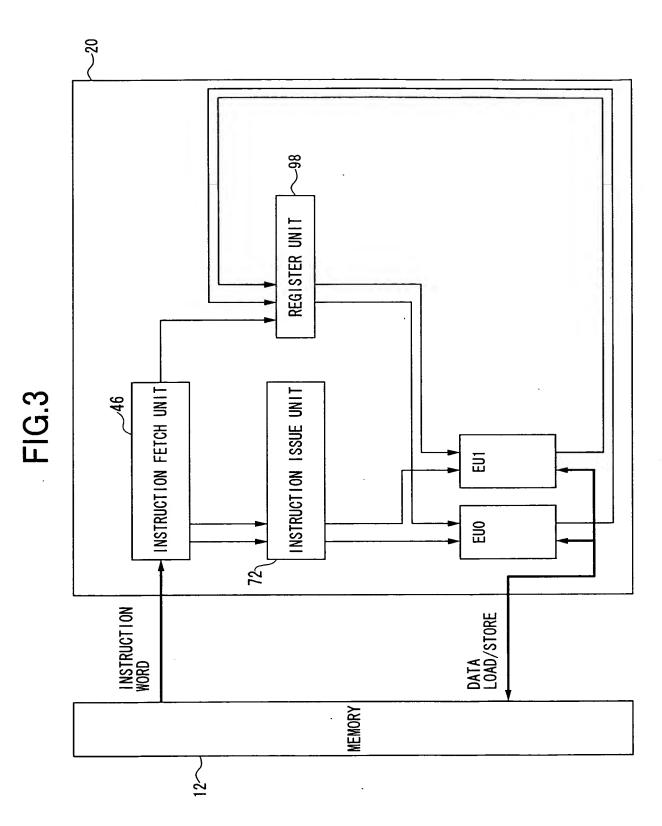


FIG.4

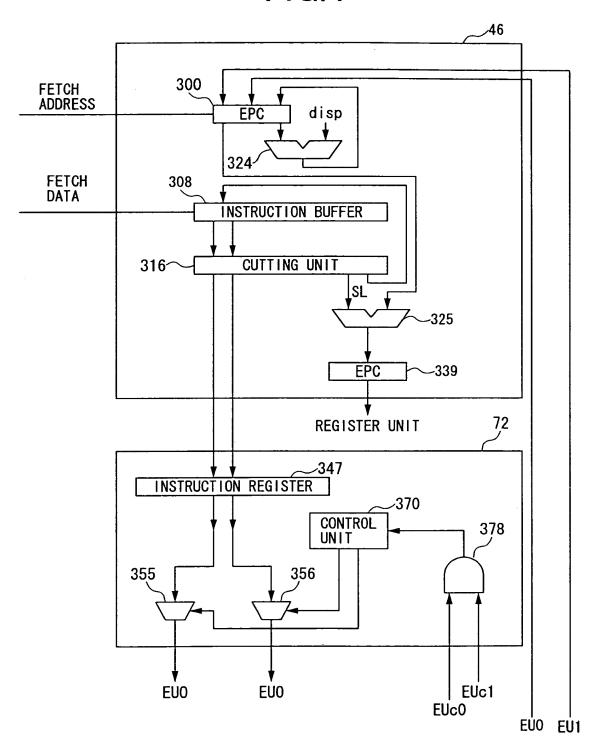
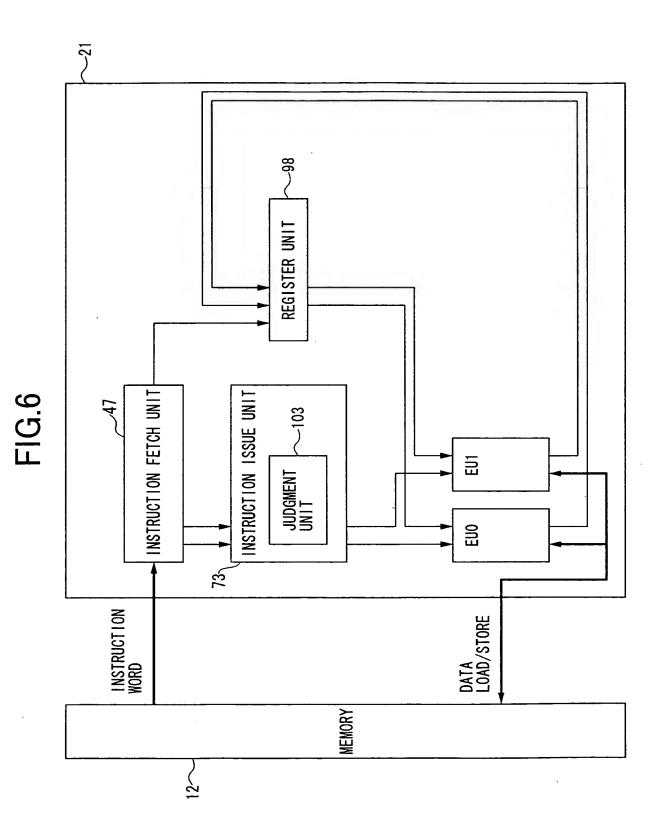


FIG. 5

0	ΕI	1	ΕI
1	EI		



~22 BRO REGISTER UNIT 딢 FIG. \equiv INSTRUCTION FETCH UNIT INSTRUCTION ISSUE UNIT CONVERSION -115 9 9 INSTRUCTION WORD DATA LOAD/STORE MEMORY

FIG.7

FIG.8

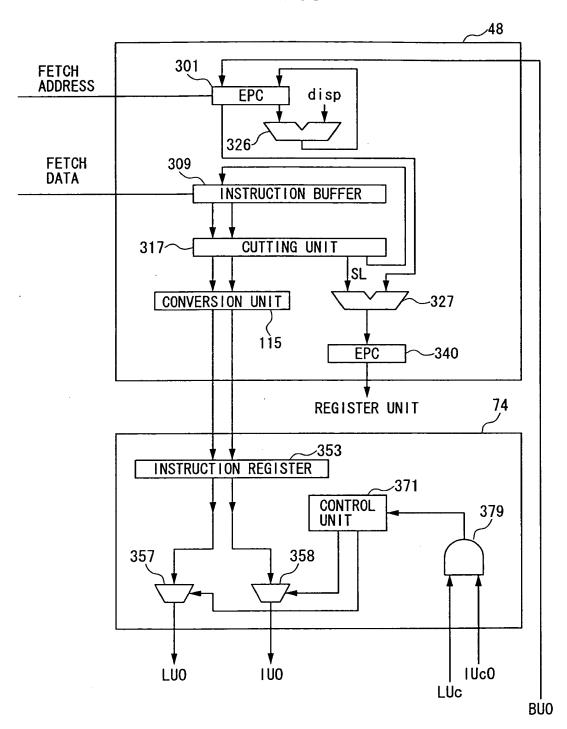
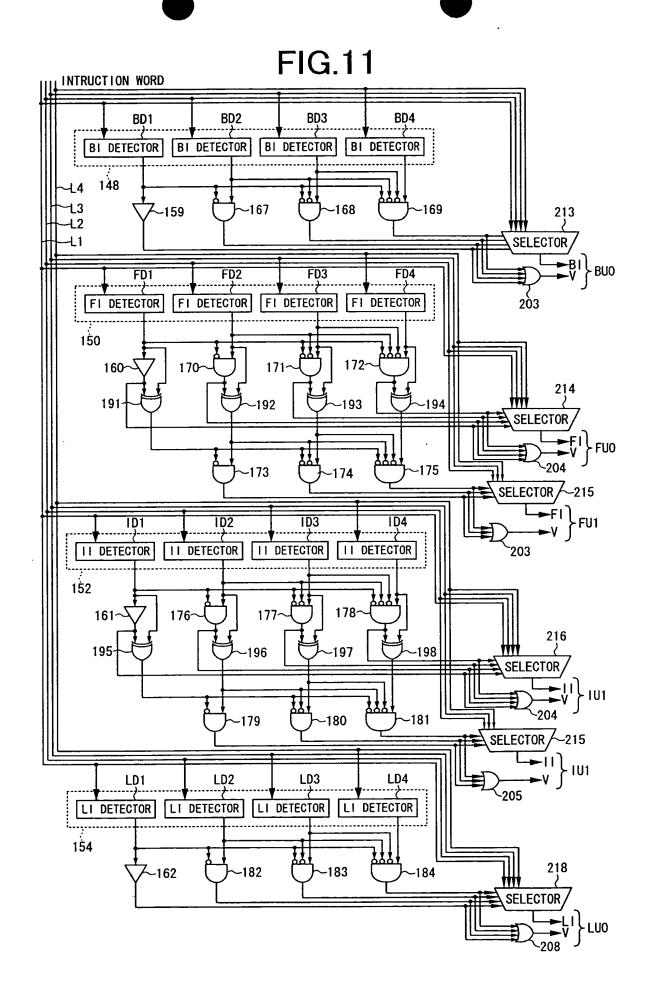


FIG. 9

		13 人		\												
FORMAT OF INSTRUCTION WORDS		LU0 IU0		IU1 FU0		0 0	FU1		BUO		7					
		٧	LI	V	11	٧	П	٧	F١	٧	FI	٧	81) 15		
0	ВІ	1	FI	0	_	0		0	_	1	۴I	0		1	ВІ	
0	ВІ	1	11	0	_	1	.11	0	-	0	ı	0	_	1	BI	
0	ВІ	1	LI	1	LI	0	_	0	1	0	1	0	_	1	ВІ	
0	FI	1	ВІ	0	-	0	_	0	1	1	FI	0	1	1	BI	
0	FI	1	FI	0	_	0	-	0	1	1	FI	1	FI	0		
0	FI	1	11	0	_	1	11	0	_	1	FI	0		0	_	
0	FI	1	LI	1	LI	0	_	0	-	1	Fl	0	_	0		
0	[]	1	ВІ	0	_	1	11	0	_	0	_	0	_	1	ВІ	
0	11	1	FI	0	_	1	Н	0	_	1	FI	0	-	0	_	> 17
0	- 11	1	11	0	_	1	11	1	11	0	-	0	_	0	_	
0	11	1	LI	1	LI	1	11	0	_	0	_	0	_	0	_	
0	LI	1	BI	1	LI	0	-	0	_	0	-	0	_	1	ВΙ	
0	LI	1	FI	1	LI	0		0	_	1	FI	0		0	_	
0	LI	1	11	1	LI	1	11	0	_	0	-	0	_	0	_	
1	ВІ			0	-	0	_	0	_	0	_	0	_	1	ВІ	
1	FI			0	_	0		0	_	1	FI	0	_	0	-	
1	11			0	_	1	11	0	_	0	_	0	_	0	_	
1	LI			1	LI	0	_	0	_	0		0	_	0	_	

FIG.10 INTRUCTION WORD BD1 BD2 BI DETECTOR BI DETECTOR 147 -163 209 ~155 SELECTOR FD1 FD2 FI DETECTOR FI DETECTOR 149 156~ 164 210 187-SELECTOR -185 ID1 ID2 II DETECTOR II DETECTOR 151 157~ 165 211 189 -190 SELECTOR -186 **-**11 -∨}101 LD1 LD2 LI DETECTOR LI DETECTOR 153 212 **-158** -166 SELECTOR



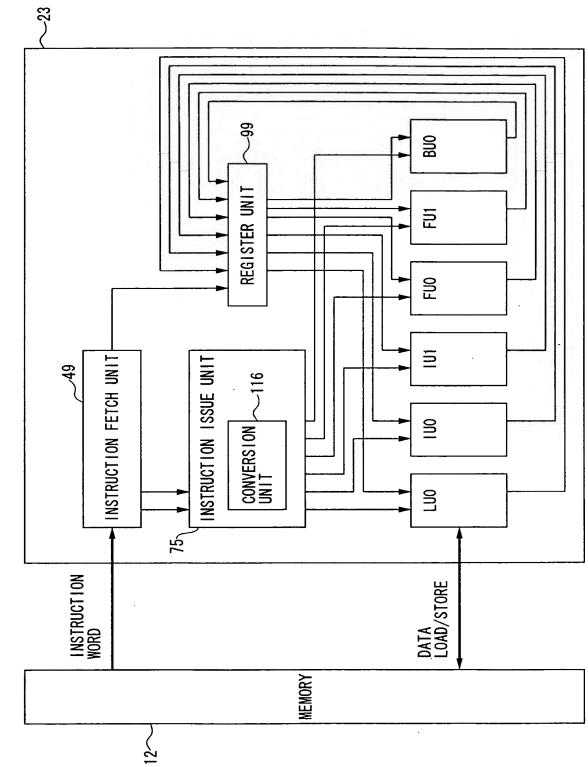
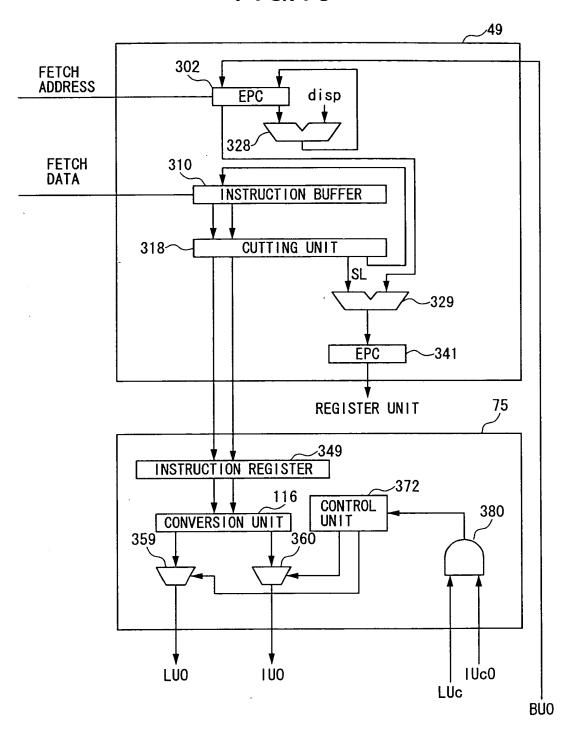


FIG.12

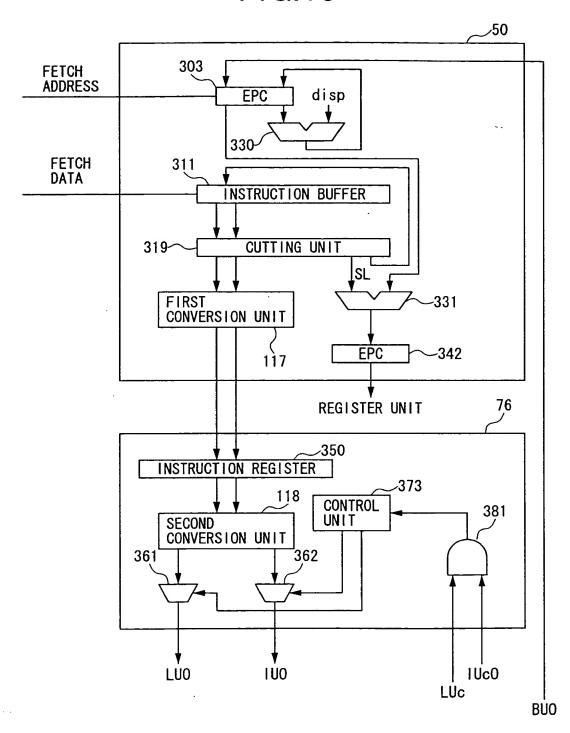
FIG.13



66 **율** REGISTER UNIT 딢 즲 ≘ INSTRUCTION FETCH UNIT INSTRUCTION ISSUE UNIT 20 SECOND CONVERSION UNIT FIRST CONVERSION UNIT <u></u> <u></u> INSTRUCTION WORD DATA LOAD/STORE MEMORY 12

FIG.14

FIG.15



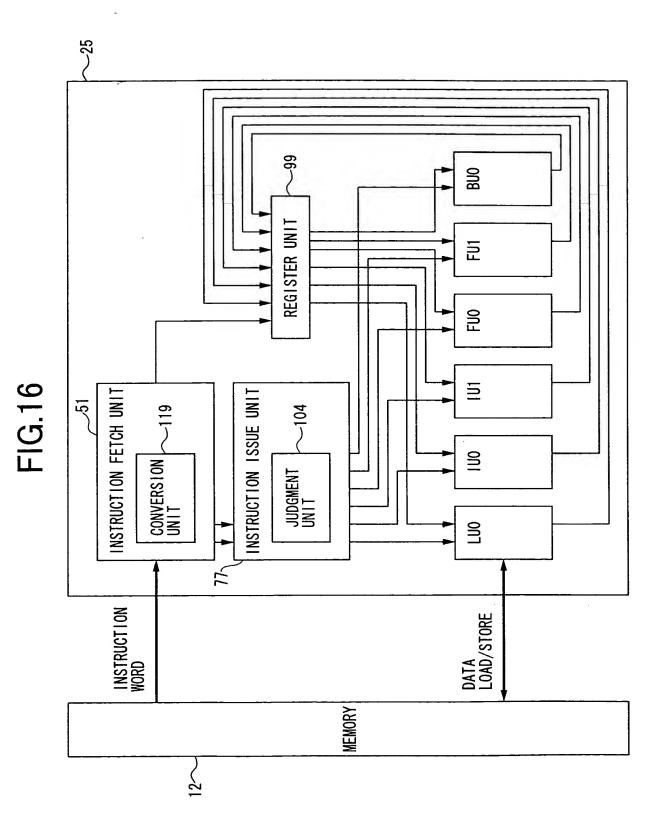
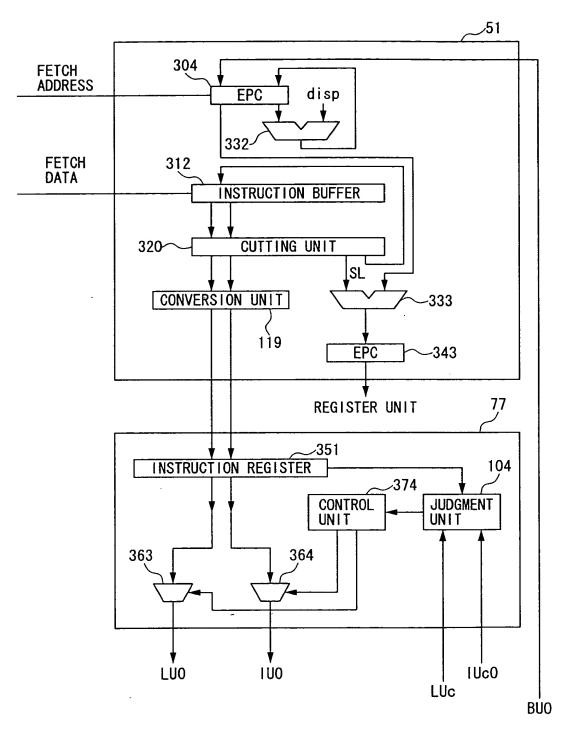


FIG.17



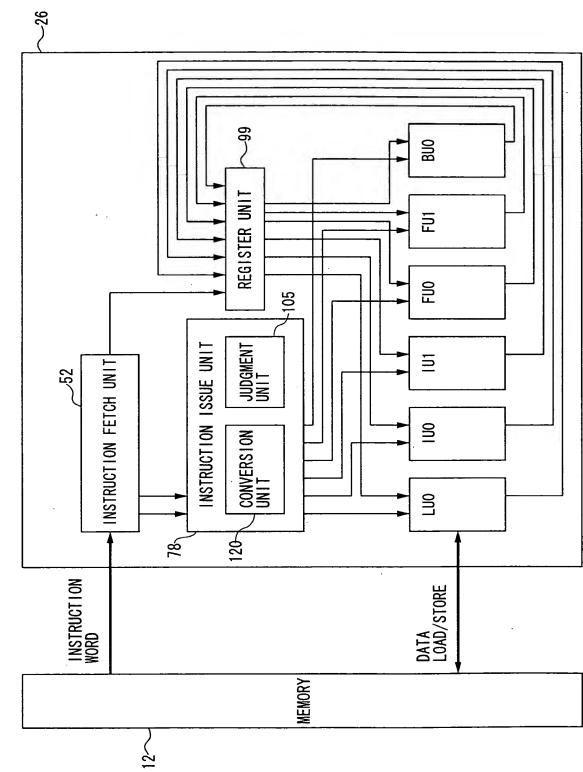
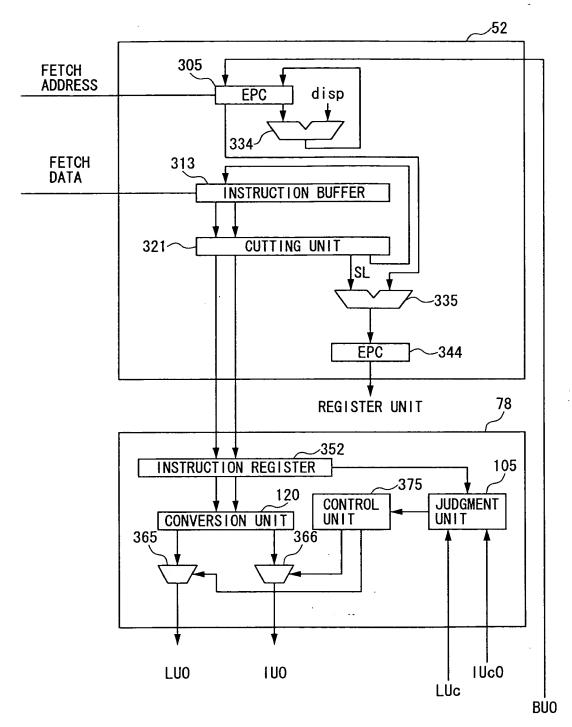


FIG.18

FIG.19



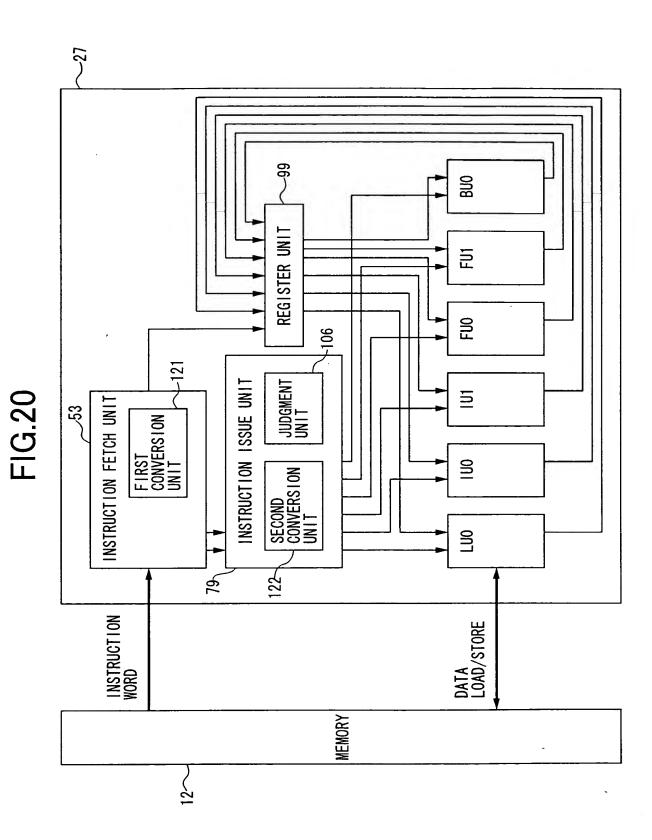
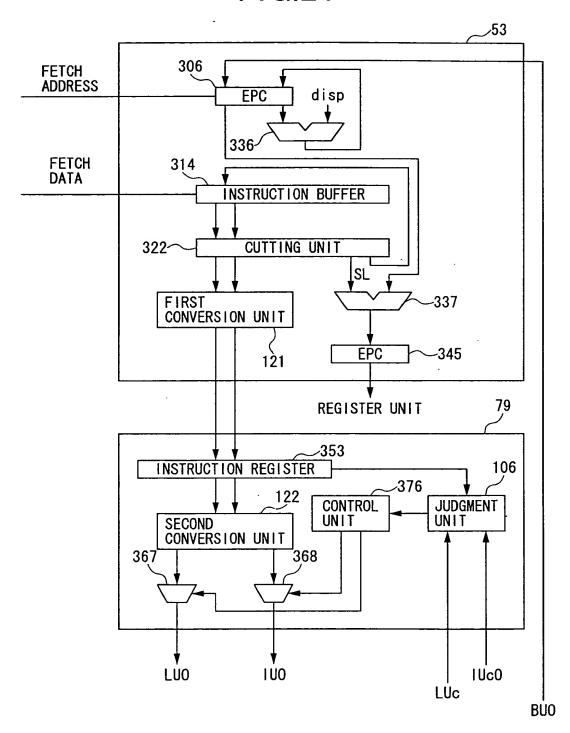


FIG.21



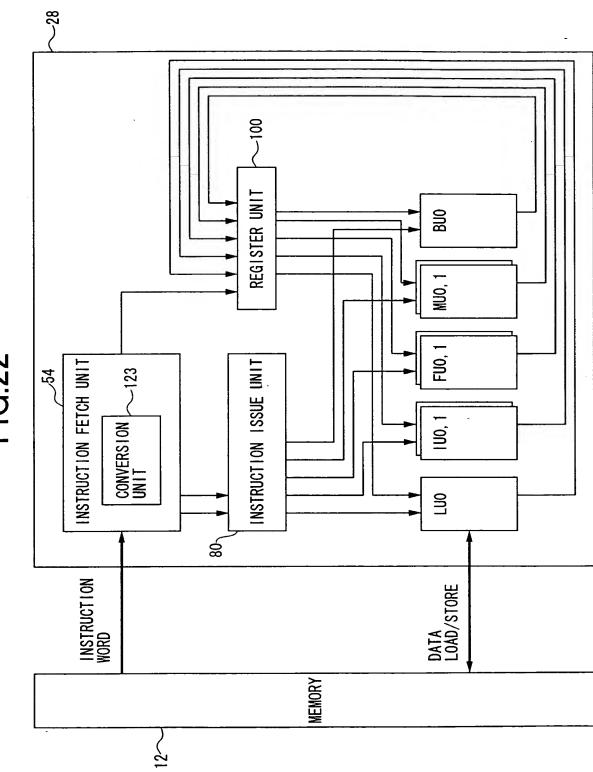
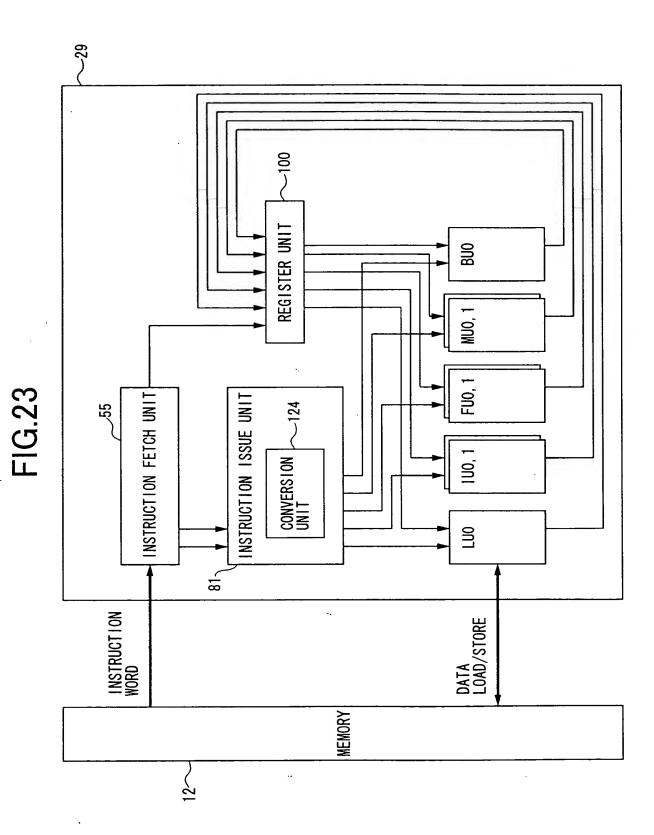
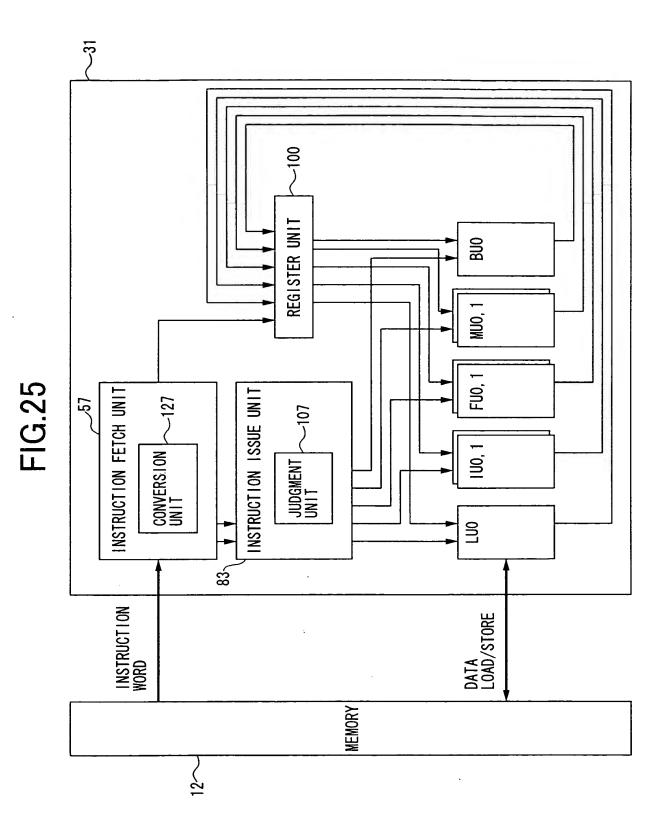
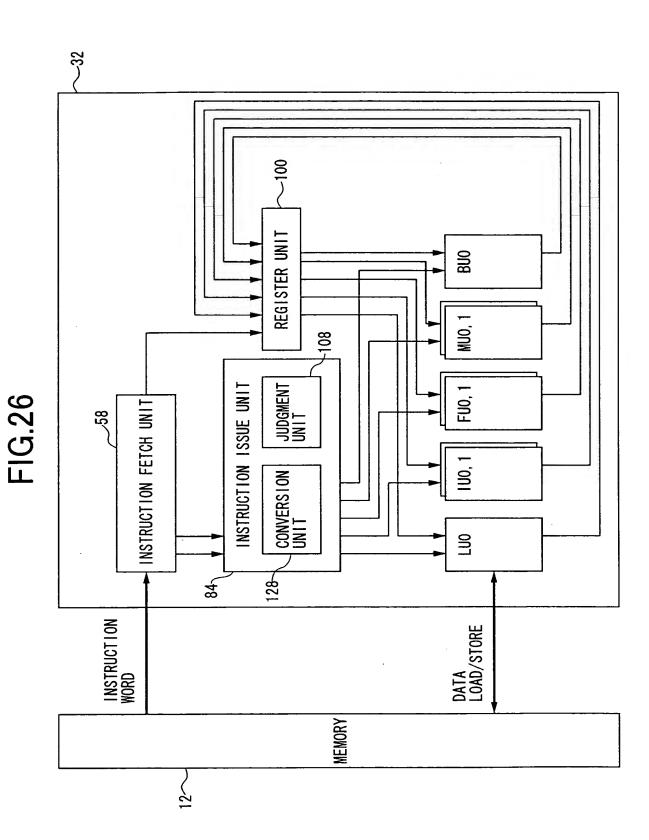


FIG.22



REGISTER UNIT BUO, 1 MU0, 1 FU0, 1 FIG.24 INSTRUCTION FETCH UNIT INSTRUCTION ISSUE UNIT 256 \sim 125 FIRST CONVERSION OUNIT SECOND CONVERSION UNIT 100,1 9 INSTRUCTION WORD DATA LOAD/STORE MEMORY 72





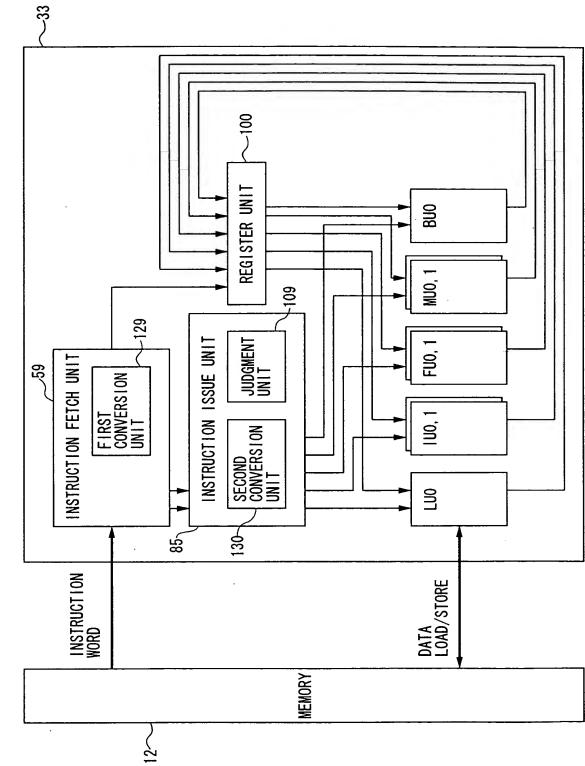
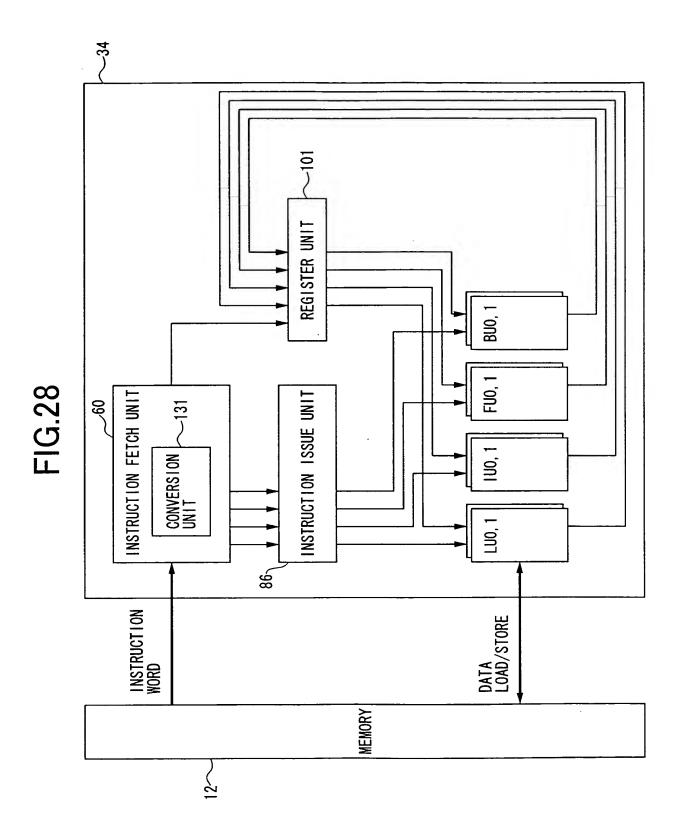
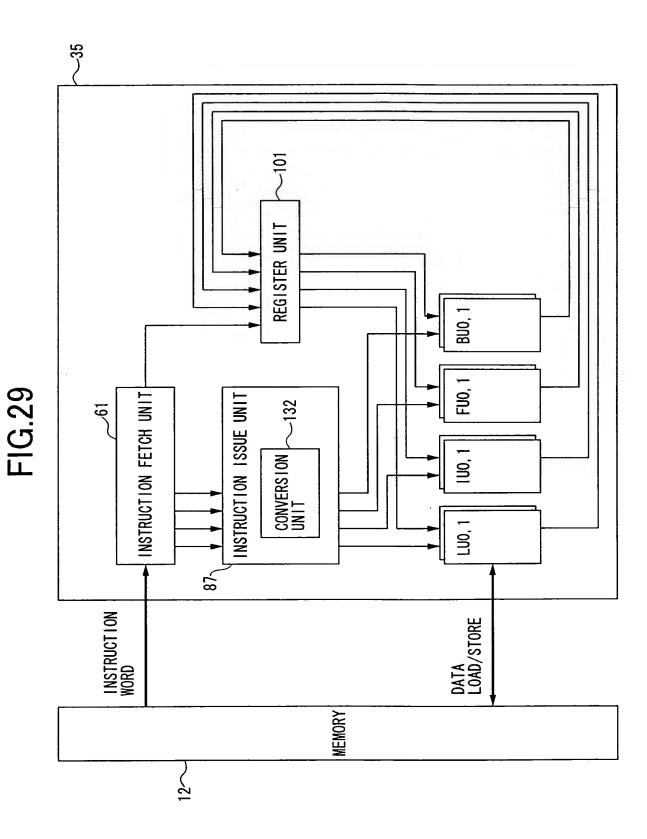
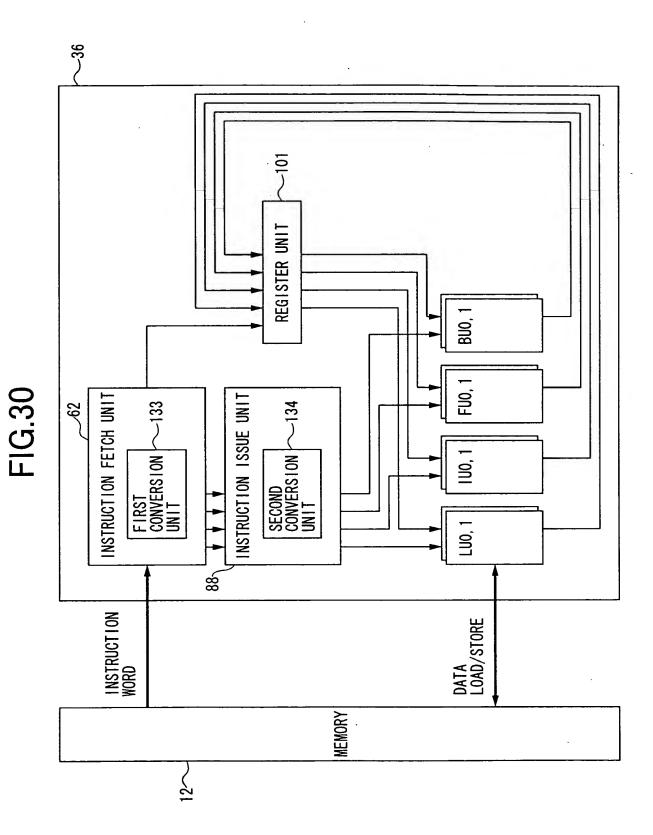


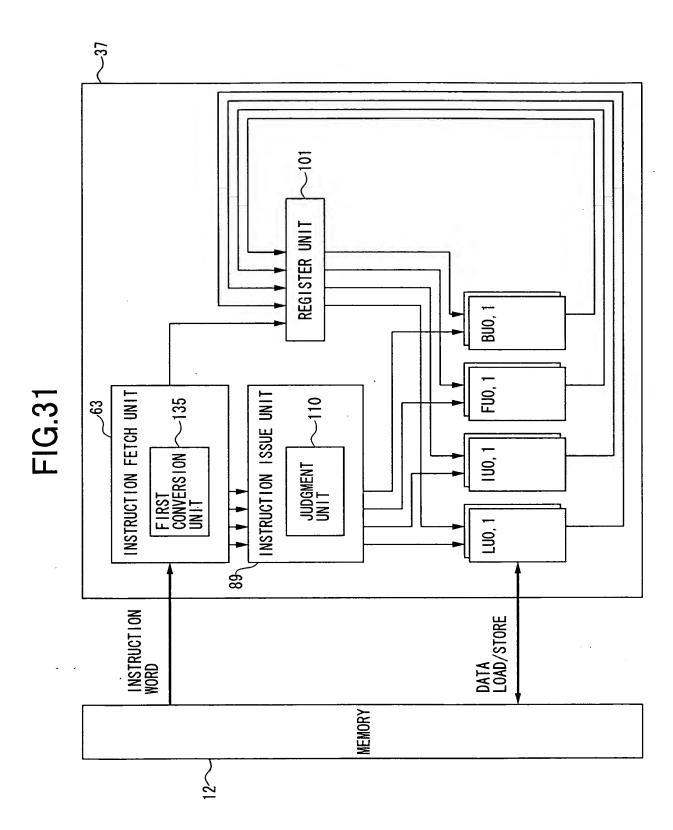
FIG.27

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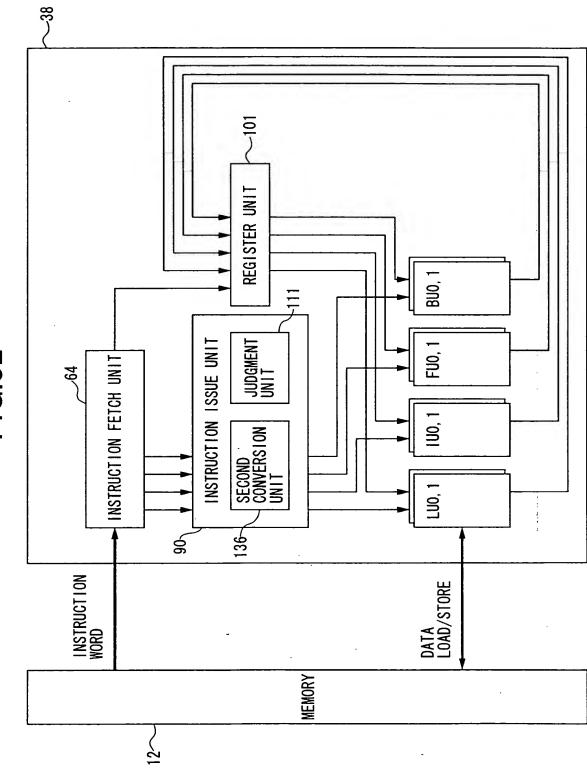
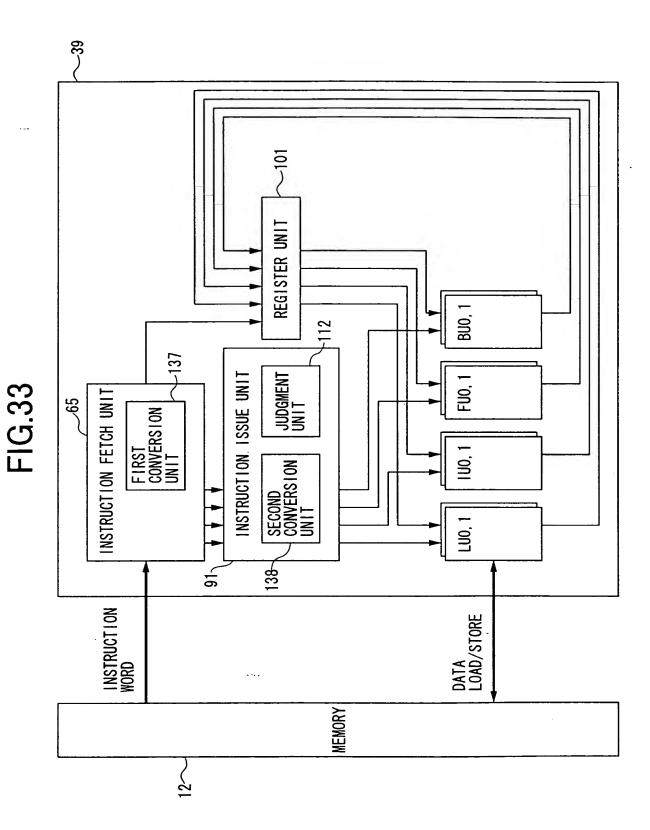


FIG.32



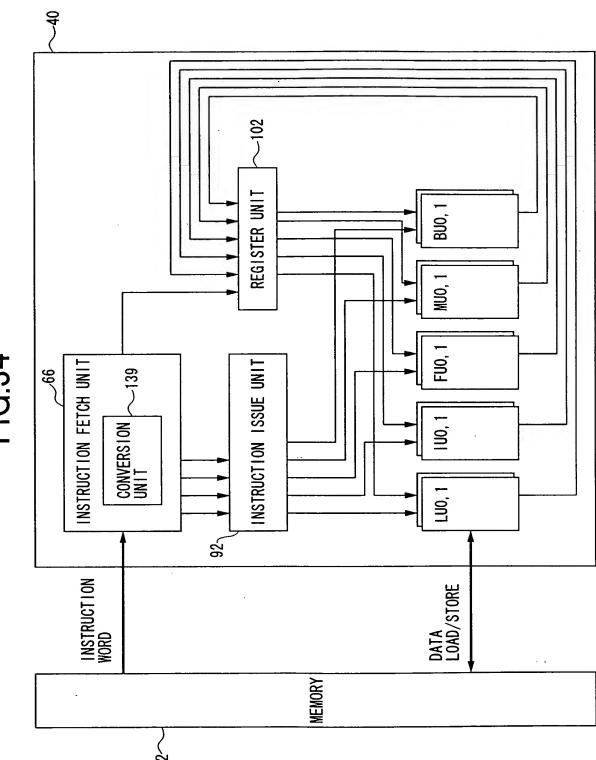


FIG.34

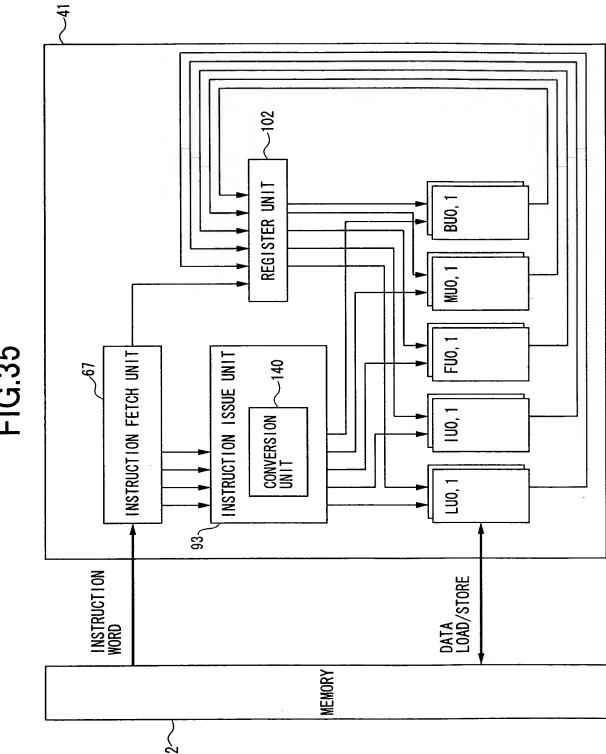


FIG.35

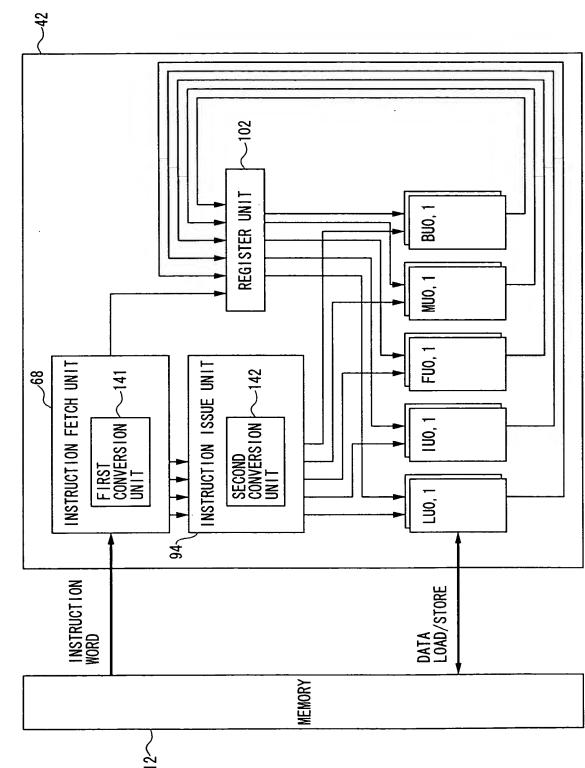
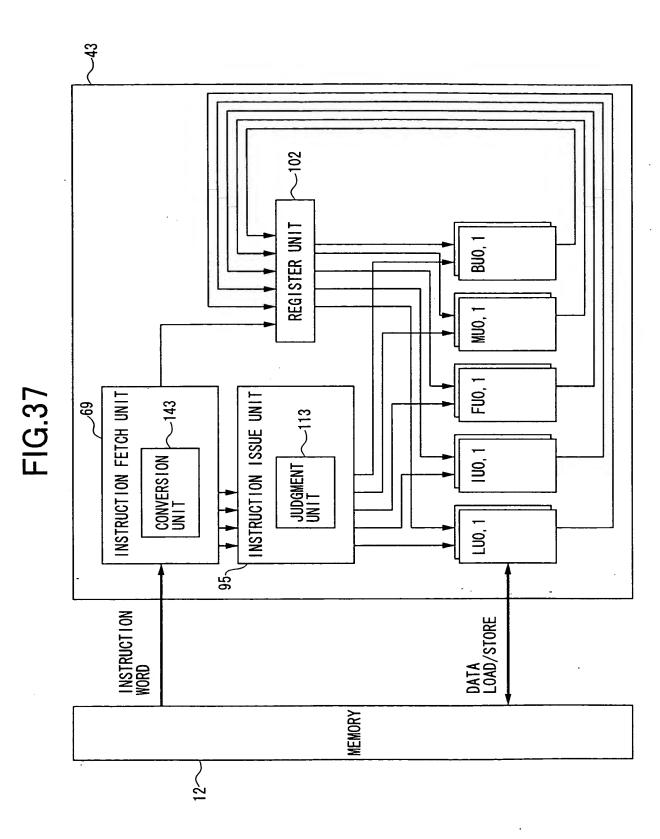


FIG.36



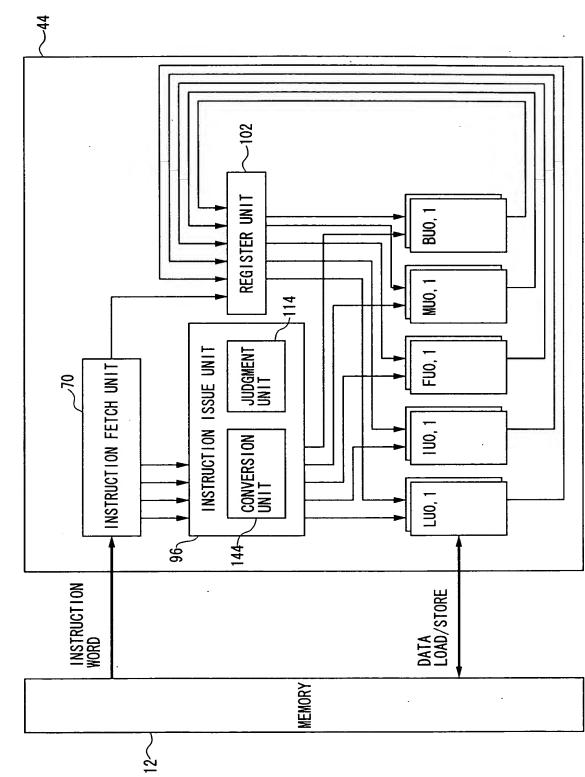


FIG.38

REGISTER UNIT BUO, 1 MU0, 1 -219 7 45 JUDGMENT UNIT FU0, 1 INSTRUCTION ISSUE UNIT INSTRUCTION FETCH UNIT FIRST CONVERSION UNIT 100, 1 SECOND CONVERSION UNIT L00, 1 146 INSTRUCTION WORD DATA LOAD/STORE MEMORY

FIG.39